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7217/71824

## IN THE BUILDED STATES PATENT AND TRADEMARK OFFICE

Applicants

Takatomo Nagamine et al.

Serial No.

10/511,733

Filed

October 18, 2004

For

VIDEO SIGNAL PROCESSING CIRCUIT AND VIDEO

SIGNAL PROCESSING METHOD

Group A.U.

2614

Examiner

I hereby certify that this paper is being deposited this date with the U.S. Postal Service as first class mail addressed to:

Commissioner for Patents,

PO Box 1450

Alexandria, VA 22313-1450

Jay H. Maioli

Date

Reg. No. 27,213

October 18, 2005

October 18, 2005 1185 Avenue of the Americas New York, NY 10036 (212) 278-0400

## INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(c)

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

As a means of complying with the duty of disclosure set forth in 37 CFR § 1.53 and in keeping with the guidelines of 37 CFR 1.98, Applicants hereby submit information thought to be relevant to the examination of the above-identified

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application, Also submitted herewith is a completed form PTO-1449.

This information was cited in a European Search Report dated September 15, 2005, and it is hereby certified that this disclosure is being made within three months of that date.

United States Patent No. 5,912,714, to Kawamura, relates to a clock generator used in a video signal processing apparatus for processing a television signal conforming to the NTSC and PAL system in order to generate a clock for determining the rate at witch picture elements (pixels) are scanned.

European Patent Application EP 0762782, Marshall, relates to a digital television receiver that receives composite television signals and provides color-separated pixel data from the television signal.

Respectfully submitted,

COOPER & DUNHAM LLE

Jay H. Maioli Reg. No. 27,213

JHM/JBG Encl.

DATE CONSIDERED

conformance and not considered. Include copy of this form with next communication to applicant.

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in

EXAMINER